IN THE UNITED STATES PATENT AND TRADEMARK OFF

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Application of

Jun OSANAI et al

: Group Art Unit - 2811 Serial No. 09/944,365

: Examiner - Thien F. Tran Filed: August 31, 2001 Jimanyor

For: COMPLEMENTARY MOS SEMI-

CONDUCTOR DEVICE AND METHOD OF MANUFACTURING

Docket No. S004-4393 THE SAME

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COMMISSIONER OF PATENTS AND TRADEMARKS Washington, DC 20231

RESPONSE

SIR:

In response to the Office Action dated August 26, 2002, applicants amend their application as follows:

ELECTION OF INVENTION:

Applicants provisionally elect, with traverse, the invention characterized by the Examiner as Group I drawn to a semiconductor device and submit that claims 1-17, 43-46, 54-55, 65 and 68-73 are readable on the elected invention.

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